



**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Shigeo YAMAZAKI, et al.

Appln. No.: 10/614,150

Confirmation No.: 7675

Filed: July 8, 2003

Docket No: Q76360

Allowed: April 14, 2006

Group Art Unit: 2114

Examiner: Loan TRUONG

For: FAULT-TOLERANT COMPUTER SYSTEM, RE-SYNCHRONIZATION METHOD  
THEREOF AND RE-SYNCHRONIZATION PROGRAM THEREOF

**SUBMISSION OF ART**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

For the possible benefit of anyone subsequently evaluating the scope and/or validity of the above-identified patent, it is requested that the documents that are listed below be placed in the U.S. Patent and Trademark Office's file wrapper of the above-identified U.S. patent:

1. Japanese Patent Application Publication No. 4-32955, published February 4, 1992, with English abstract (copy enclosed).
2. Japanese Patent Application Publication No. 10-326199, published December 8, 1998, previously submitted in the Information Disclosure Statement filed April 20, 2006 (copy not enclosed).
3. Japanese Patent Application Publication No. 4-247531, published September 3, 1992, previously submitted in the Information Disclosure Statement filed April 20, 2006 (copy not enclosed).

The above-listed documents were recently cited in a corresponding Japanese Office Action, dated June 21, 2006, a copy of which is enclosed.

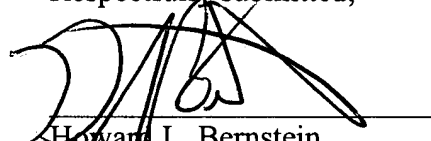
SUBMISSION OF ART  
U.S. Appln. No.: 10/614,150

Atty. Docket No.: Q76360

The undersigned has not reviewed the teachings of the above-listed document in detail and thus makes no representations concerning the relevancy or materiality of the above-listed document.

This is not an Information Disclosure Statement and no response from the U.S. Patent and Trademark Office is believed to be necessary, nor are any fees believed to be due.

Respectfully submitted,



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WASHINGTON OFFICE

**23373**

CUSTOMER NUMBER

Date: July 26, 2006